

Fig.1

TrCH	PhCH	Allocation of TS and codes	Repetition period	Time to foresee activation of the PhCH
DCH	DPCH	RRC signaling	—	Depend on processing time of UTRAN
BCH	P-CCPCH	Fixedly using TS0, $C_{16}^{(1)}$ and $C_{16}^{(2)}$	Repeating a time every 8, 16, 32, 64 and even more frames	≥ 8 frames
PCH	S-CCPCH	BCH broadcasts	—	Depend on processing time of UTRAN
FACH	S-CCPCH	BCH broadcasts	—	$< 3000\text{ms}$ (default)
—	PICH	BCH broadcasts	Default value: repeating 2 frames every 64 frames.	Fixed by BCH
DSCH	PDSCH	BCH broadcasts	—	Depend on processing time of UTRAN
—	FPACH	BCH broadcasts	Only occupying one traffic TS	≤ 4 subframes

Fig.2

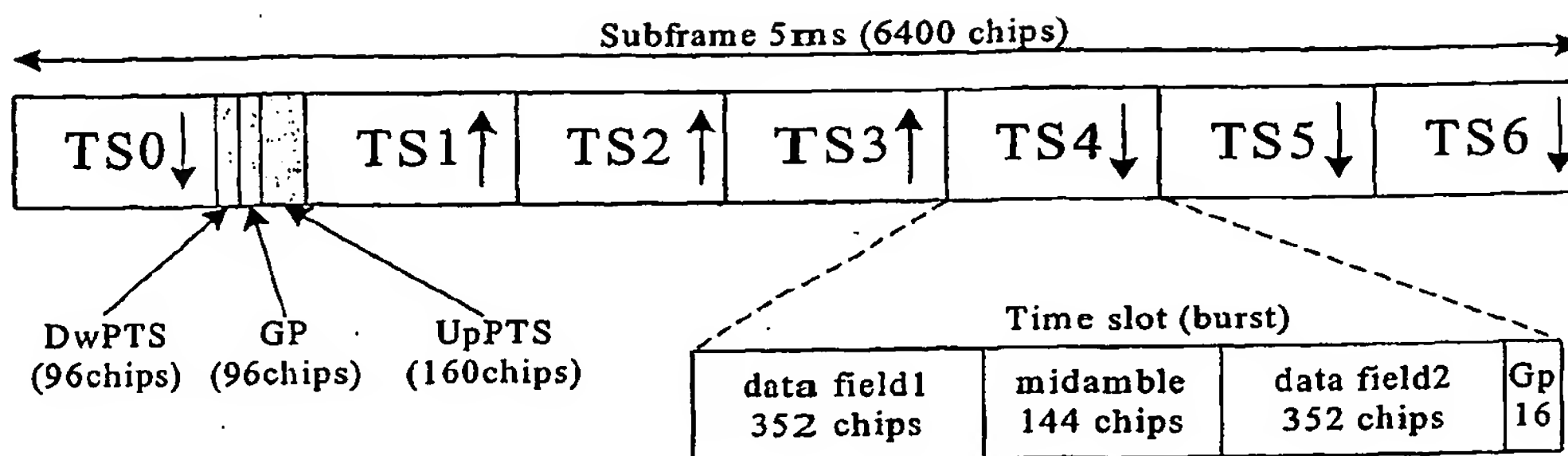


Fig.3

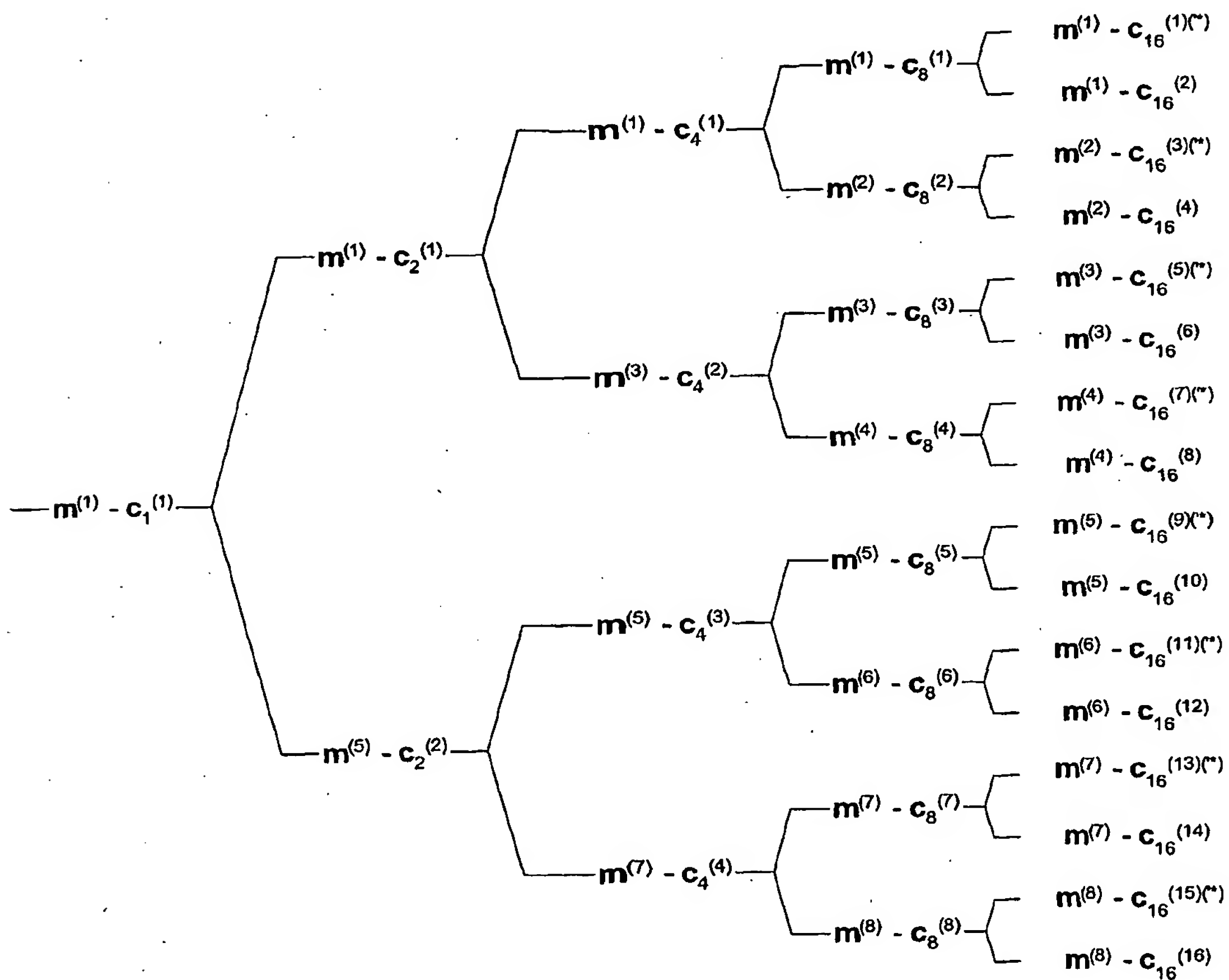


Fig.4

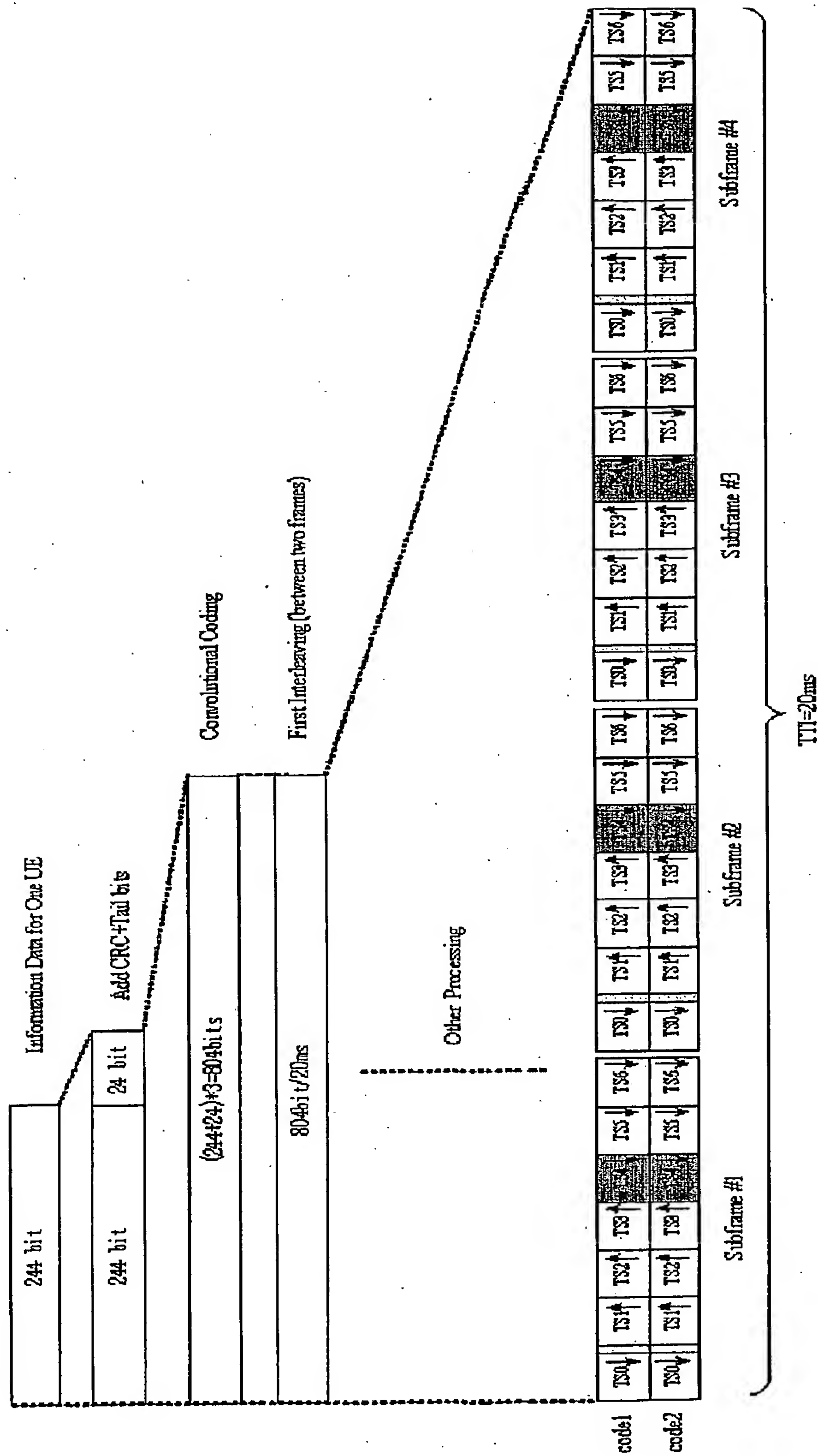


Fig. 5

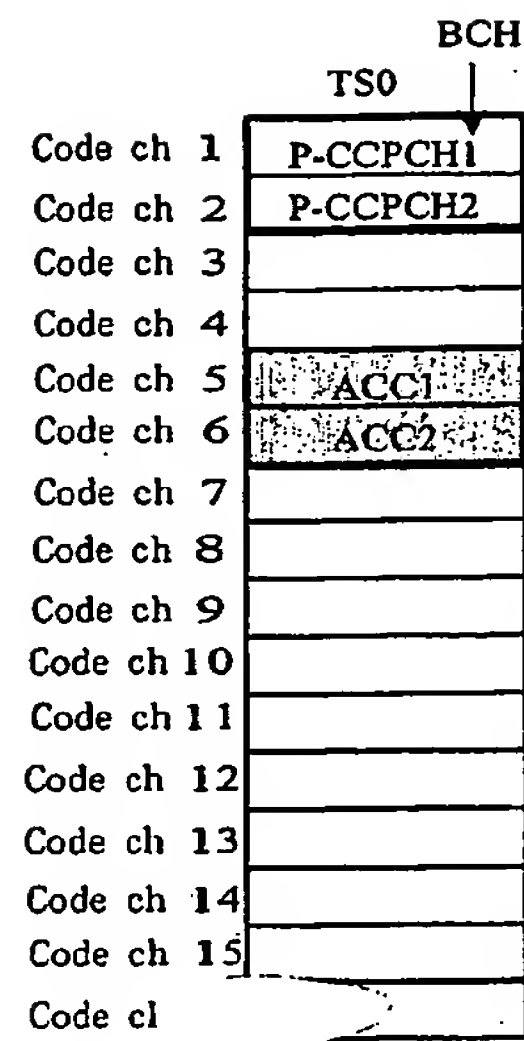


Fig.6

TrCH	PhCH	Allocation of TS and codes	Demodulation	Acquisition of ACC for other PhCHs in TS
BCH	P-CCPCH	Fixed	JD	Derived from Identified Midambles and the fixed codes allocation
—	ACC	Fixed	JD	Derived from Identified Midambles and the fixed codes allocation
—	PICH	BCH broadcasts	JD	From ACC channel in TS0
PCH	S-CCPCH	BCH broadcasts	JD	From ACC channel in TS0
FACH	S-CCPCH	BCH broadcasts	JD	From ACC channel in TS0
—	FPACH	BCH broadcasts	JD	From ACC channel in TS0
DSCH	PDSCH	BCH broadcasts	JD	From ACC channel in TS0
DCH	DPCH	RRC signaling	JD	From ACC channel in TS0

Fig.7

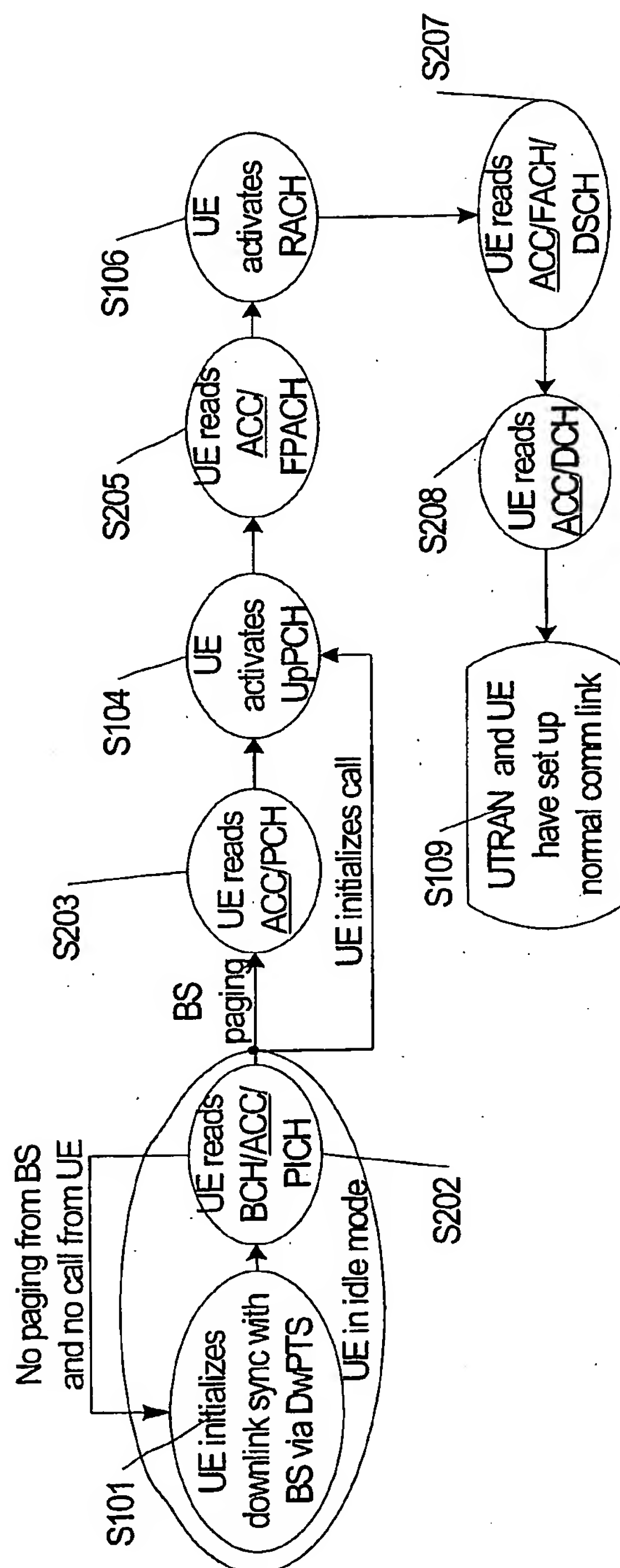


Fig.8

TrCH	PhCN	Allocation of TS and codes	Demodulation	Acquisition of ACC for other PhCHs	Other requirements
BCH	P-CCPCH	Fixed	Rake	Unnecessary	High Tx power
—	PICH	BCH broadcasts	Rake	Unnecessary	Time division duplex with BCH and high Tx power
PCH	S-CCPCH	BCH broadcasts	Rake	Unnecessary	Time division duplex with BCH and high Tx power
—	FPACH	BCH broadcasts	Rake	Unnecessary	High Tx power
—	ACC	Fixed	JD	Derived from identified midambles and the bitmap in FPACH	Demodulate FPACH first to get the secondary codes info of TS0 in next frame, then ACC dedicated channel in the next frame to get ACC info of subsequent frame; read ACC info of every frame afterwards
FACH	S-CCPCH	BCH broadcasts	JD	From ACC dedicated channel in TS0	No
DSCH	PDSCH	BCH broadcasts	JD	From ACC dedicated channel in TS0	No
DCH	DPCH	RRC signaling	JD	From ACC dedicated channel in TS0	No

Fig.9

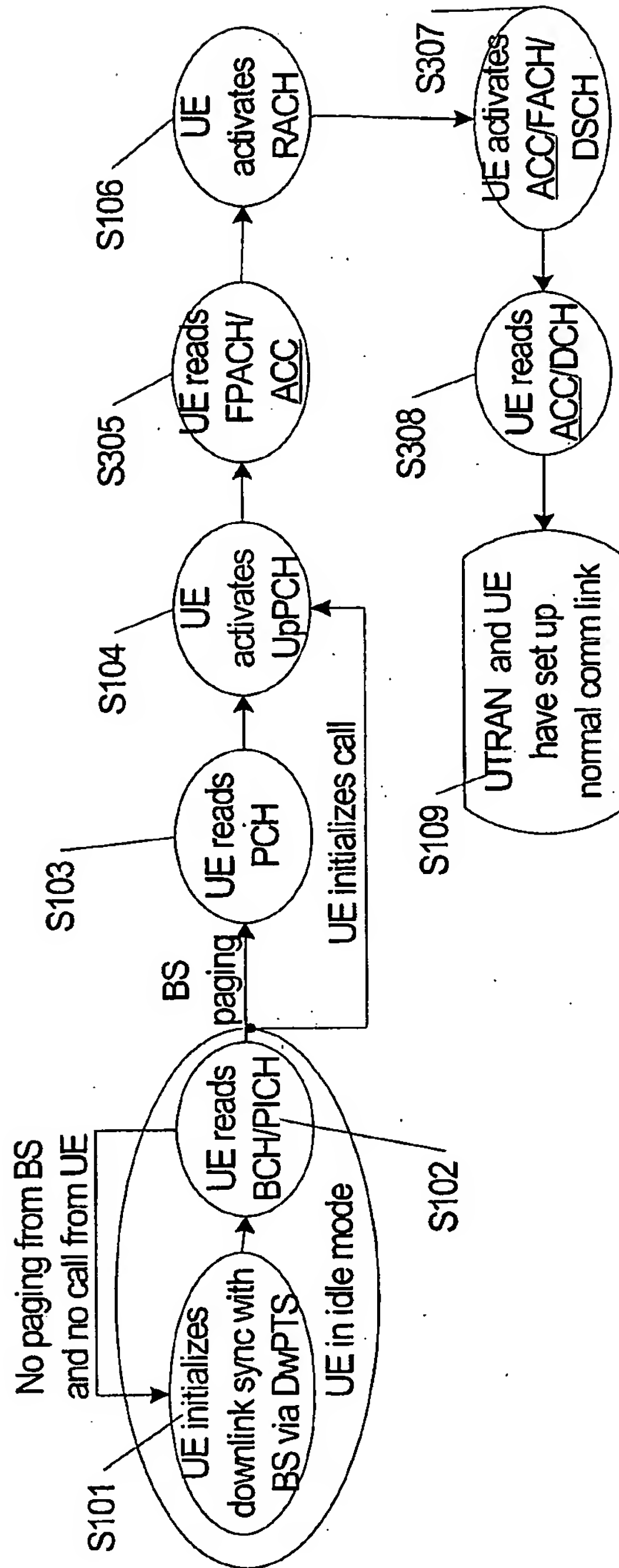


Fig.10

$m^{(1)}$	$m^{(2)}$	$m^{(3)}$	$m^{(4)}$	$m^{(5)}$	$m^{(6)}$	$m^{(7)}$	$m^{(8)}$	The number of corresponding channelisation codes
1	0	0	0	0	0	0	0	1 or 9
0	1	0	0	0	0	0	0	2 or 10
0	0	1	0	0	0	0	0	3 or 11
0	0	0	1	0	0	0	0	4 or 12
0	0	0	0	1	0	0	0	5 or 13
0	0	0	0	0	1	0	0	6 or 14
0	0	0	0	0	0	1	0	7 or 15
0	0	0	0	0	0	0	1	8 or 16

Fig.11

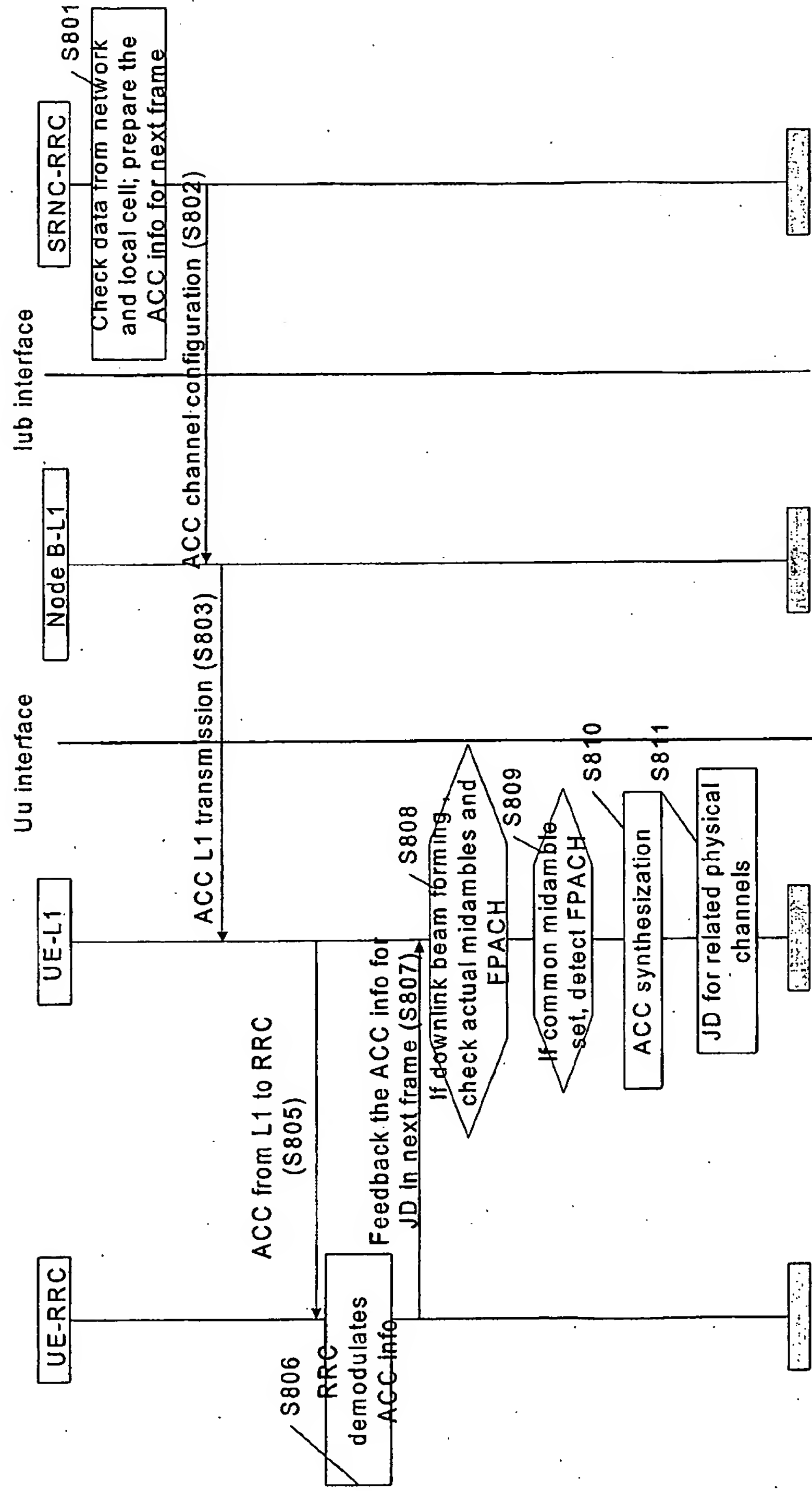


Fig.12

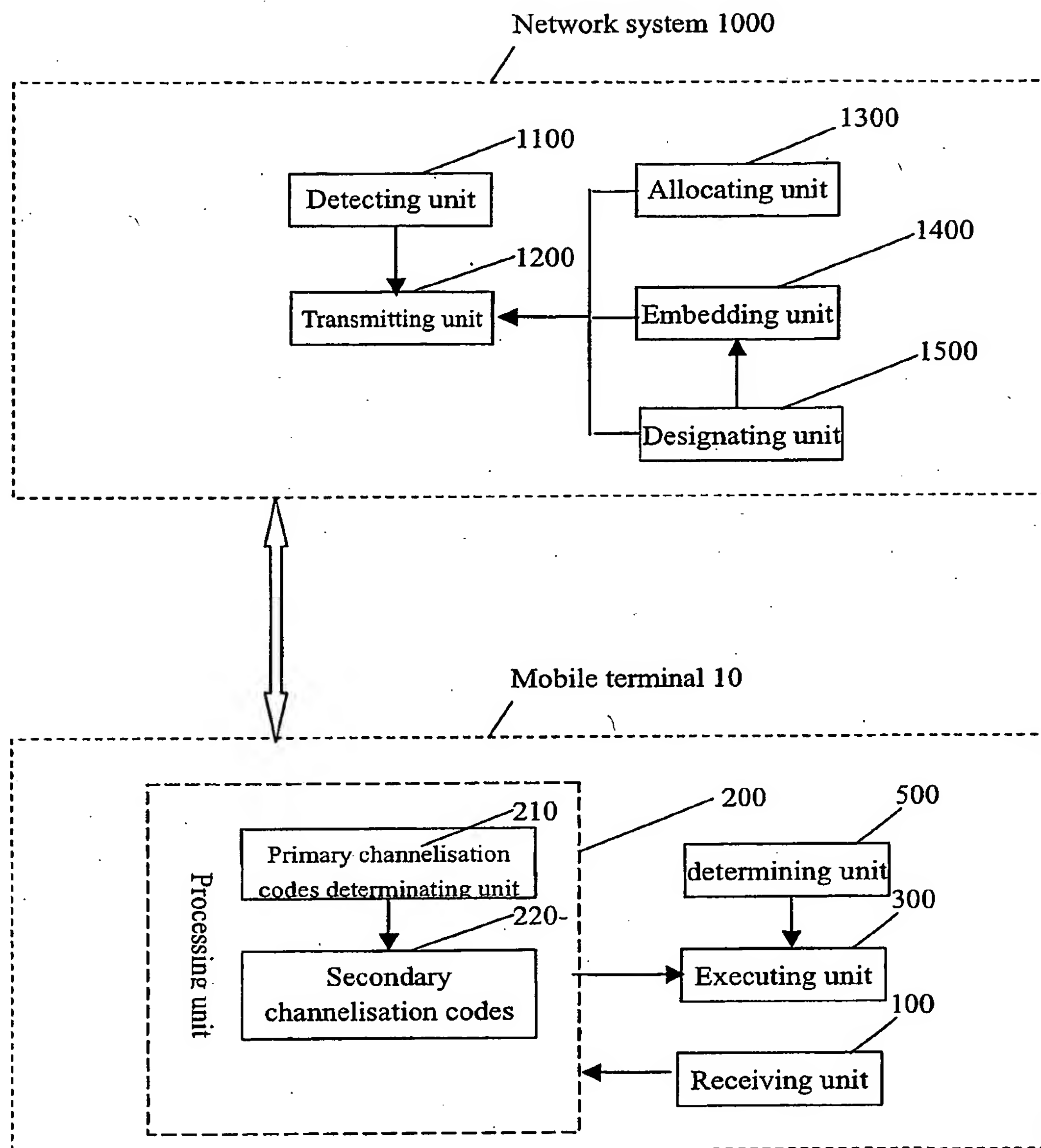


Fig.13